BAS40-04LT1G, SBAS40-04LT1G

Dual Series Schottky Barrier Diode

These Schottky barrier diodes are designed for high speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand held and portable applications where space is limited.

Features

- Extremely Fast Switching Speed
- Low Forward Voltage
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Reverse Voltage	V _R	40	V
Forward Power Dissipation @ T _A = 25°C Derate above 25°C	P _F	225 1.8	mW mW/°C
Operating Junction and Storage Temperature Range	T _{J,} T _{stg}	-55 to +150	°C
Forward Continuous Current	I _{FM}	120	mA
Single Forward Current t ≤ 1 s t ≤ 10 ms	I _{FSM}	200 600	mA
Thermal Resistance (Note 1) Junction–to–Ambient (Note 2)	$R_{\theta JA}$	508 311	°C/W

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. FR-4 @ minimum pad.
- 2. FR-4 @ 1.0 x 1.0 in pad.



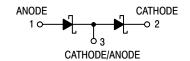
ON Semiconductor®

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40 VOLTS SCHOTTKY BARRIER DIODES



SOT-23 (TO-236) CASE 318 STYLE 11



MARKING DIAGRAM



CB = Specific Device Code

M = Date Code*■ Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
BAS40-04LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
SBAS40-04LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel

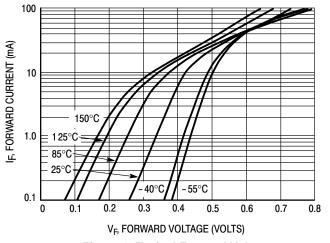
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

BAS40-04LT1G, SBAS40-04LT1G

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
Reverse Breakdown Voltage (I _R = 10 μA)	V _{(BR)R}	40	_	V
Total Capacitance (V _R = 1.0 V, f = 1.0 MHz)	Ст	_	5.0	pF
Reverse Leakage (V _R = 25 V)	I _R	_	1.0	μΑ
Forward Voltage (I _F = 1.0 mA)	V _F	_	380	mV
Forward Voltage (I _F = 10 mA)	V _F	-	500	mV
Forward Voltage (I _F = 40 mA)	V _F	_	1.0	V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



T_A = 150°C IR, REVERSE CURRENT (uA) 125°C 10 85°C 1.0 0.1 25°C 0.01 0.001 5.0 20 25 10 15 V_R, REVERSE VOLTAGE (VOLTS)

Figure 1. Typical Forward Voltage

Figure 2. Reverse Current versus Reverse Voltage

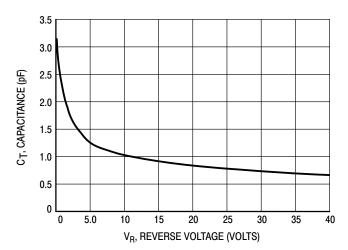


Figure 3. Typical Capacitance



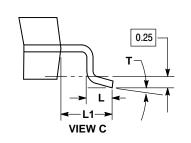


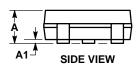
SOT-23 (TO-236) CASE 318-08 **ISSUE AS**

DATE 30 JAN 2018

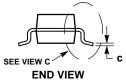
SCALE 4:1 D Ε <-- 3X b

TOP VIEW

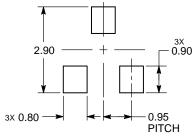




3. CATHODE



RECOMMENDED SOLDERING FOOTPRINT



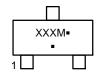
DIMENSIONS: MILLIMETERS

3. ANODE

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH.
 MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH,
- PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
С	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
Т	O٥		100	٥٥		100

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

= Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 7: PIN 1. EMITTER 2. BASE 3. COLLECTOR	STYLE 8: PIN 1. ANODE 2. NO CONNECTION 3. CATHODE		
STYLE 9: PIN 1. ANODE 2. ANODE 3. CATHODE	STYLE 10: PIN 1. DRAIN 2. SOURCE 3. GATE	STYLE 11: PIN 1. ANODE 2. CATHODE 3. CATHODE-ANODE	STYLE 12: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 13: PIN 1. SOURCE 2. DRAIN 3. GATE	STYLE 14: PIN 1. CATHODE 2. GATE 3. ANODE
STYLE 15: PIN 1. GATE 2. CATHODE 3. ANODE	STYLE 16: PIN 1. ANODE 2. CATHODE 3. CATHODE	STYLE 17: PIN 1. NO CONNECTION 2. ANODE 3. CATHODE	STYLE 18: PIN 1. NO CONNECTION 2. CATHODE 3. ANODE	STYLE 19: PIN 1. CATHODE 2. ANODE 3. CATHODE-ANODE	STYLE 20: PIN 1. CATHODE 2. ANODE 3. GATE
STYLE 21: PIN 1. GATE 2. SOURCE 3. DRAIN	STYLE 22: PIN 1. RETURN 2. OUTPUT 3. INPUT	STYLE 23: PIN 1. ANODE 2. ANODE 3. CATHODE	STYLE 24: PIN 1. GATE 2. DRAIN 3. SOURCE	STYLE 25: PIN 1. ANODE 2. CATHODE 3. GATE	STYLE 26: PIN 1. CATHODE 2. ANODE 3. NO CONNECTION
STYLE 27: PIN 1. CATHODE 2. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE				

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98ASB42226	В

PAGE 2 OF 2

AJ AK AL	REVISION ADDED STYLE 27. REQ. BY P. LEM. OBSOLETED -09 VERSION. REQ. BY D. TRUHITTE. ADDED NOMINAL VALUES AND UPDATED GENERIC MARKING DIAGRAM. REQ. BY HONG XIAO.	07 JUL 2004 14 SEP 2004
AK	OBSOLETED -09 VERSION. REQ. BY D. TRUHITTE. ADDED NOMINAL VALUES AND UPDATED GENERIC MARKING DIAGRAM. REQ.	14 SEP 2004
	ADDED NOMINAL VALUES AND UPDATED GENERIC MARKING DIAGRAM. REQ.	
AL		27 1441/ 2005
		27 MAY 2005
AM	REDREW LEAD SIDE VIEW. REQ BY DARRELL TRUHITTE.	26 AUG 2005
AN	REINTRODUCED LABELS FOR DIMENSION C. REQ. BY D. TRUHITTE.	14 OCT 2005
AP	ADDED THETA DEGREE VALUES TO DIMENSION TABLE. REQ. BY D. TRUHITTE.	17 NOV 2009
AR	MODIFIED DIMENSIONS C AND L. REQ. BY M. YOU.	10 OCT 2016
AS	ADDED STYLE 28. REQ. BY E. ESTILLER.	30 JAN 2018

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